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CONFIRMATION NO. 7396

SERIAL NUMBER 10/600,737	FILING DATE 06/20/2003  RULE	CLASS <del>483</del> 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 31638US4	
<b>APPLICANTS</b> Satoshi Ishikura, Osaka, JAPAN;					
<b>** CONTINUING DATA *****</b> This application is a DIV of 10/278,496 10/23/2002 PAT 6,611,950 which is a DIV of 10/080,740 02/22/2002 PAT 6,502,225 which is a DIV of 09/274,659 03/23/1999 PAT 6,421,816					
<b>** FOREIGN APPLICATIONS *****</b> JAPAN HEI. 10-94993 04/07/1998 NO					
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> <b>** 09/24/2003</b>					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged	<input checked="" type="checkbox"/> Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 3/4
<b>ADDRESS</b> 00116 PEARNE & GORDON LLP 1801 EAST 9TH STREET SUITE 1200 CLEVELAND , OH 44114-3108					
<b>TITLE</b> Semiconductor device, semiconductor device design method, semiconductor device design method recording medium, and semiconductor device design support system					
FILING FEE	FEES: Authority has been given in Paper		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of		